IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Gary Tessman, Jr. et al.   Art Unit   : 2611
Serial No. : 10/007,696   Examiner : Unknown
Filed   : December 10, 2001
Title   : DISTRIBUTED IMAGE STORAGE ARCHITECTURE **RECEIVED**

Commissioner for Patents
Washington, D.C. 20231

MAY 2 2 2002

Technology Center 2600

TRANSMITTAL LETTER

Correspondence relating to this application is enclosed. The required fees are computed below. Please apply any charges not covered, or any credits, to Deposit Account No. 06-1050.

Total Claims	59	-	22	=	37	$666
Independent	5	-	5	=	0	$0
First Presentation of Multiple Dependent Claims						$0

Applicant hereby petitions under 37 C.F.R. §1.136 for a 0 month
extension of time. $0

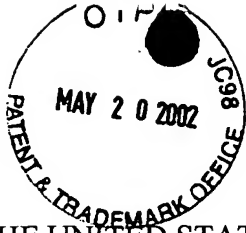TOTAL FEE DUE $666

A check for $666 is attached.

Respectfully submitted,

Date: _May 20, 2002_

Scott R. Boalick
Reg. No. 42,337

Fish & Richardson P.C.
1425 K Street, N.W.
11th Floor
Washington, DC 20005-3500
Telephone: (202) 783-5070
Facsimile: (202) 783-2331

40097901.doc

Attorney's Docket No.: 06975-172001 / Communications 43

7/A

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Gary Tessman, Jr. et al.   Art Unit : 2661
Serial No. : 10/007,696   Examiner : Unknown
Filed   : December 10, 2001
Title   : DISTRIBUTED IMAGE STORAGE ARCHITECTURE

Commissioner for Patents
Washington, D.C. 20231

PRELIMINARY AMENDMENT

Prior to examination, please amend the application as follows:

In the specification:

Replace the paragraph beginning at page 5, line 17 with the following rewritten paragraph:

-- Referring to Fig. 2, the communications system 200 is an expansion of the block diagram of Fig. 1, focusing primarily on one particular implementation of the host system 20. The host system 20 includes a host device 22 and a controller 24. The host controller 24 generally is capable of transmitting instructions to any or all of the elements of the host device 22. For example, in one implementation, the host controller 24 includes one or more software applications loaded on the host device 22. In other implementations, the host controller 24 may include any of several other programs, machines, and devices operating independently or collectively to control the host device 22. --

Replace the paragraph beginning at page 7, line 3 with the following rewritten paragraph:

-- In the implementation shown by Fig. 2, the OSP host complex 230 includes a routing processor 232. In general, the routing processor 232 will examine an address field of a data request, use a mapping table to determine the appropriate destination for the data request, and direct the data request to the appropriate destination. More specifically, in a packet-based implementation, the client system 10 may generate information requests, convert the requests into data packets, sequence the data packets, perform error checking and other packet-switching